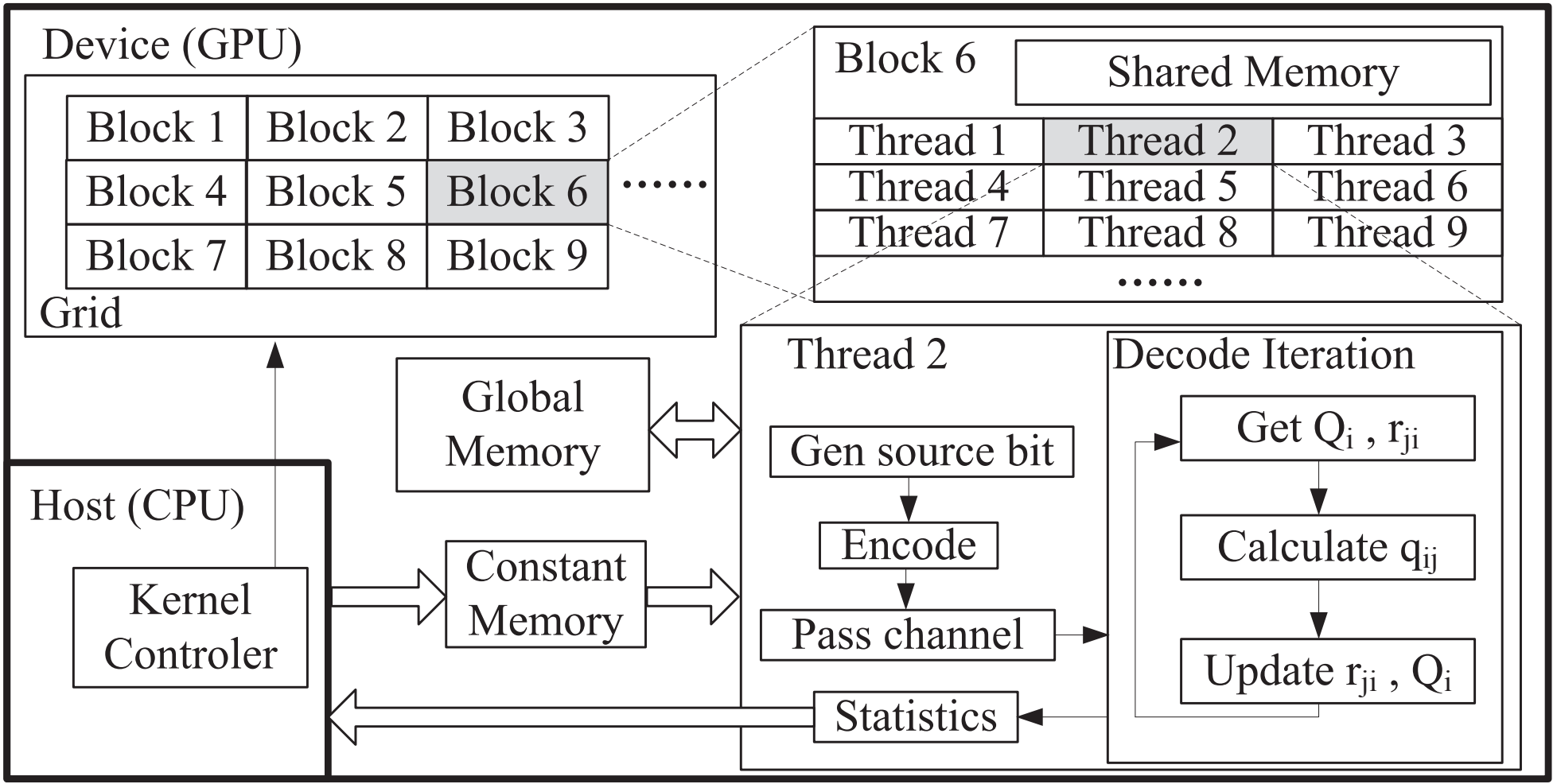
Device (GPU)
Block 1
Block 2
Block 3
Block 4
Block 5
Block 6
Block 7
Block 8
Block 9
Grid
Block 6
Shared Memory
Thread 1
Thread 2
Thread 3
Thread 4
Thread 5
Thread 6
Thread 7
Thread 8
Thread 9
Global Memory
Host (CPU)
Kernel Controler
Constant Memory
Thread 2
Gen source bit
Encode
Pass channel
Statistics
Decode Iteration
Get Q_i , r_{ji}
Calculate q_{ij}
Update r_{ji} , Q_i