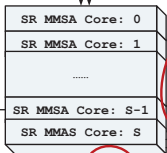
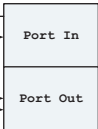


Execution Part

Interface Part



LLRSUM
LOOP

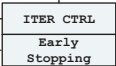
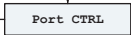


cfg
din
dout

LLREX
LOOP



00..0



Control Part

